

MONDAY, NOVEMBER 18 - 8:30 - 9:00am

# Opening Session & Award Presentations

Room: Almaden 1 & 2

## Opening Remarks -

Jörg Henkel - General Chair - *Karlsruhe Institute of Technology (KIT)*

## Award Presentations

### IEEE/ACM William J. McCalla ICCAD Best Paper Award

This award is given in memory of William J. McCalla for his contribution to ICCAD and his CAD technical work throughout his career.

*Front End*

#### 2B.1 Improved SAT-based ATPG: More Constraints, Better Compaction

Stephan Eggersglüß, Robert Wille, Rolf Drechsler - *Univ. of Bremen / DFKI*

*Back End*

#### 5B.2 Methodology for Standard Cell Compliance and Detailed Placement for Triple Patterning Lithography

Bei Yu, Xiaoqing Xu, Jih-Rong Gao and David Z. Pan - *Univ. of Texas at Austin*

### ICCAD Ten Year Retrospective Most Influential Paper Award

This award is being given to the paper judged to be the most influential on research and industrial practice in computer-aided design of integrated circuits over the ten years since its original appearance at ICCAD.

#### 2003 Paper Titled: Statistical Timing Analysis Considering Spatial Correlations Using A Single PERT-Like Traversal

Authors: Hongliang Chang, Sachin S. Sapatnekar - *Univ. of Minnesota*

Publication: ICCAD 2003 Proceedings: pp. 621-625

### IEEE CEDA Early Career Award

David Atienza, *Assistant Professor of Electrical Engineering and Director of the Embedded Systems Laboratory at EPFL*

For sustained and outstanding contributions to design methods and tools for multi-processor systems-on-chip (MPSoC), particularly for work on thermal-aware design, low-power architectures and on-chip interconnects synthesis.

### IEEE CEDA Early Career Award

Zhuo Li, *IBM T. J. Watson Research Center*

For essential and outstanding contributions to algorithms, methodologies, and software for interconnect optimization, physical synthesis, parasitic extraction, testing and simulation.

### IEEE CEDA Outstanding Service Contribution

Alan Hu, *Univ. of British Columbia*

For significant service to the EDA community as the 2012 ICCAD General Chair

### 2013 SIGDA Pioneering Achievement Award

Donald E. Thomas, *Dept. of Electrical and Computer Engineering, Carnegie Mellon Univ.*

For his pioneering work in making the Verilog Hardware Description Language more accessible for the design automation community and allowing for faster and easier pathways to simulation, high-level synthesis, and co-design of hardware-software systems.

### ACM/SIGDA CADathlon

Introduction of the 2013 winners.