

Best Paper Candidates/Award Committee

Best Paper Award Committee

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IEEE/ACM William J. McCalla ICCAD Best Paper Award Candidates

Monday Sessions

1A.1 LAYOUT DECOMPOSITION FOR TRIPLE PATTERNING LITHOGRAPHY

Bei Yu, Boyang Zhang, Duo Ding, David Z. Pan - *Univ. of Texas, Austin*
Kun Yuan - *Cadence Design Systems, Inc.*

2A.1 A SIMPLR METHOD FOR ROUTABILITY-DRIVEN PLACEMENT

Jin Hu, Myung-Chul Kim, Dong-Jin Lee, Igor L. Markov -
Univ. of Michigan

2B.2 OPTIMAL STATISTICAL CHIP DISPOSITION

Vladimir Zolotov, Jinjun Xiong - *IBM T.J. Watson Research Ctr.*

2C.1 IMPROVING SHARED CACHE BEHAVIOR OF MULTITHREADED OBJECT-ORIENTED APPLICATIONS IN MULTICORES

Mahmut Kandemir, Shekhar Srikantaiah - *Pennsylvania State Univ.*
Seung Woo Son - *Argonne National Lab*

Tuesday Sessions

4A.1 MGR: MULTI-LEVEL GLOBAL ROUTER

Yue Xu, Chris Chu - *Iowa State Univ. and Cadence Design Systems, Inc.*
Chris Chu - *Iowa State Univ.*

7B.1 MODEL ORDER REDUCTION OF FULLY PARAMETERIZED SYSTEMS BY RECURSIVE LEAST SQUARE OPTIMIZATION

Zheng Zhang, Luca Daniel - *Massachusetts Institute of Technology*
Ibrahim (Abe) M. Elfadel - *Masdar Institute of Science and Tech.*

Wednesday Sessions

9B.1 FORMAL VERIFICATION OF PHASE-LOCKED LOOPS USING REACHABILITY ANALYSIS AND CONTINUIZATION

Matthias Althoff, Akshay Rajhans, Bruce Krogh, Soner Yaldiz, Xin Li,
Larry Pileggi - *Carnegie Mellon Univ.*

9C.1 CACTI-P: ARCHITECTURE-LEVEL MODELING FOR SRAM-BASED STRUCTURES WITH ADVANCED LEAKAGE REDUCTION TECHNIQUES

Sheng Li, Norm Jouppi - *Hewlett-Packard Labs.*
Ke Chen - *Univ. of Notre Dame and Hewlett-Packard Labs.*
Jay Brockman - *Univ. of Notre Dame*
Jung Ho Ahn - *Seoul National Univ.*

10A.1 GATE SIZING AND DEVICE TECHNOLOGY SELECTION ALGORITHMS FOR HIGH-PERFORMANCE INDUSTRIAL DESIGNS

Muhammet Mustafa Ozda, Steven Burns - *Intel Corp.*
Jiang Hu - *Texas A&M Univ.*